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**PATENT** Attorney Docket No. ASC-042

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANTS:** 

Fitzgerald et al.

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**EXAMINER:** 

W. Lindsay, Jr.

TITLE:

GATE TECHNOLOGY FOR STRAINED SURFACE CHANNEL

AND STRAINED BURIED CHANNEL MOSFET DEVICES

Box Issue Fee Commissioner for Patents Washington, D.C. 20231

## TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Transmitted herewith for filing in the above-referenced patent application are twelve (12) sheets of formal drawings corresponding to FIGS 1A, 1B, 2A, 2B, 3A, 3B, 3C, 3D, 4A, 4B, 4C, 4D, 5, 6, 7, 8, 9, and 10.

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